

**UNITED STATES PATENT APPLICATION**

**OF**

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**FOR**

**METHOD OF FABRICATING IRIIDIUM-BASED MATERIALS AND  
STRUCTURES ON SUBSTRATES, AND IRIIDIUM SOURCE REAGENTS  
THEREFOR**

**CROSS-REFERENCE TO RELATED APPLICATION**

**This is a divisional of U.S. Application Serial No. 09/453,995, filed December 3, 1999, now allowed; which is a continuation-in-part of U.S. Patent Application No. 08/966,797 filed November 10, 1998 in the names of Thomas H. Baum and Chongying Xu for "Method of Fabricating Iridium-Based Materials and Structures on Substrates, and Iridium Source Reagents Therefor," now allowed.**

## **BACKGROUND OF THE INVENTION**

### **Field Of The Invention**

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This invention relates to a method of forming iridium- or iridium-containing materials on substrates, such as Ir-based electrode structures for microelectronic devices and subassemblies, as well as to Ir source reagent materials, and novel dielectric capacitor or ferroelectric material device structures.

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### **Description of the Related Art**

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Iridium (Ir) and iridium oxide ( $\text{IrO}_2$ ) are of great interest for use as electrode materials in both dynamic random access memories (DRAMs) and for ferroelectric-based memory devices (FRAMs) which incorporate perovskite metal oxide thin-films as the capacitor layer.

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The advantages of Ir over other possible electrode materials include ease of deposition, the ability to “dry etch” the material, the ability to form a stable conducting oxide at high temperatures in an oxidizing environment, and the ability to operate reliably at high temperatures in a working device.

The deposition and processing of Ir-based electrodes is highly desirable in view of the aforementioned advantages. Further, the formation of  $\text{IrO}_2$  acts as a diffusion barrier to

oxidation of conducting polysilicon vias or plugs, as is required in high density DRAM or FRAM devices.

Based on the need for Ir-based electrodes, the art has continued to seek improvements in  
5 source materials and deposition techniques for the formation of Ir-based films.

The art has variously disclosed the chemical vapor deposition of iridium for the manufacture of electronic devices in a reducing atmosphere, such as hydrogen gas environment. The art has taught the use of such reducing atmosphere for the purpose of  
10 achieving the deposition of element metal iridium for electrodes in applications in which high temperature dielectric materials (e.g., SBT, BST, PZT, PLZT, PNZT, LaCaMnO<sub>3</sub>, etc., wherein SBT = strontium bismuth tantalate, BST = barium strontium titanate, PZT = lead zirconium titanate, PLZT = lead lanthanum zirconium titanate, PNZT = lead niobium zirconium titanate) are deposited on the electrode, to minimize the possibility of  
15 degradation of the dielectric in such applications and to concurrently achieve the formation of high purity metal.

The art has especially sought improvements in process technology for the formation of semiconductor and ferroelectric structures that employ Ir electrodes specifically  
20 associated with complex dielectric or ferroelectric material layers as thin-film capacitors.

It is an object of the present invention to provide novel source reagents and a process for the formation of iridium-based electrodes that achieve a material simplification in fabrication efficiency and cost, and provide an electrode structure that is highly

advantageous for integration with silicon device technology, being efficient, oxygen reactive, oxygen impermeable, and readily fabricated.

It is another object of the invention to provide a simplified method for the fabrication of metal oxide thin film capacitor structures including iridium, iridium oxide or iridium-containing electrode elements, as metal contacts for the oxide DRAM and FRAM devices.

Other objects and advantages of the present invention will be more fully apparent from the ensuing disclosure and appended claims.

### **SUMMARY OF THE INVENTION**

This invention relates to a method of forming iridium- or iridium-containing materials on substrates, such as Ir-based electrode structures for microelectronic devices and subassemblies, and catalytic materials, as well as to Ir source reagent materials, and novel dielectric material structures.

As used herein, the term “Ir-based” or “iridium-based” refers broadly to elemental iridium, iridium oxide, iridium-containing material compositions and combinations thereof.

The present invention also relates to novel high temperature dielectric or ferroelectric thin film capacitor structures including Ir-based electrode elements.

In one aspect, the invention relates to a method of forming an iridium-containing film on a substrate, from an iridium-containing precursor thereof that is decomposed to deposit iridium on the substrate, such method comprising decomposing the precursor and depositing iridium on the substrate in an oxidizing ambient environment. The deposition of iridium on the substrate may be carried out in any suitable manner and by any appropriate techniques of the art, including chemical vapor deposition (CVD), assisted CVD, or physical deposition methods such as ion plating, rapid thermal processing, molecular beam epitaxy, etc.

As used herein, the term "oxidizing ambient environment" means an environment including oxygen-containing gas, such as oxygen, ozone, air, nitrogen oxide ( $\text{NO}_x$ ), or the like. Such oxidizing atmosphere may be provided in a deposition chamber or reaction vessel in which the deposition is carried out, and enables the formation of iridium or iridium oxide on the substrate. Accordingly, the deposition may be conducted in an ambient air environment, thereby simplifying the formation of the iridium-containing film on the substrate. In an alternate embodiment,  $\text{IrO}_2$  may be formed in a post-deposition process from Ir metal by treatment in an oxidizing environment.

The Ir precursor material may be of any suitable composition and type. In preferred practice of the present invention, the precursor may suitably comprise a Lewis base-

stabilized  $\beta$ -diketonate iridium composition or a Lewis base-stabilized beta-ketoiminate composition, as hereafter more fully described.

When the iridium-containing film is employed to form an electrode or other patterned structure on the substrate, the deposited iridium or iridium oxide film may be dry etched with a halogen-based plasma and/or preferably,  $\text{XeF}_2$ , as more fully described in concurrently filed U.S. Patent Application No. 08/966,796 filed November 10, 1997 in the names of Thomas H. Baum and Frank DiMeo, Jr., for "Method for Etch Fabrication of Iridium-Based Electrode Structures," the disclosure of which hereby is incorporated herein in its entirety. In such dry etching of a deposited iridium or iridium oxide film, the etch rates can optionally be enhanced through the use of Lewis-based adducts or electron back-bonding species such as carbon monoxide, trifluorophosphine, trialkylphosphines or other suitable Lewis base.

In yet another aspect of the present invention, the iridium-containing film subsequent to its formation as an electrode structure may have deposited thereon a high temperature dielectric and/or ferroelectric material. An oxidizing ambient environment may be employed for the deposition of the iridium-containing film or may be used solely during the deposition of the oxide dielectric/ferroelectric.

It may therefore be unnecessary to purge the chamber of a reducing atmosphere, or to transfer the substrate article bearing the iridium-containing film from the iridium deposition chamber to a dielectric/ferroelectric deposition chamber, as has been done in

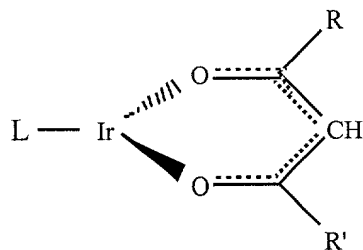
the prior art to accommodate the usage of hydrogen or other reducing gas (forming gas) atmospheres in the iridium electrode formation step.

The method of this invention therefore achieves a substantial simplification of the procedure for forming a capacitor or other microelectronic device in which the iridium-containing electrode is overcoated with a dielectric or ferroelectric material.

Another aspect of the invention relates to a microelectronic device structure comprising an iridium oxide electrode element overcoated by a high temperature dielectric, e.g., SBT, PZT, BST, PLZT, PNZT, LaCaMnO<sub>3</sub>, etc., wherein the electrode is conductively operative in relation to the high temperature dielectric. As used herein, high temperature dielectric refers to a dielectric material deposited on the electrode at a temperature above about 300°C. By way of example, dielectric films of lead zirconium titanate (PZT) are typically deposited at temperatures on the order of 500-600°C.

Yet another aspect of the invention relates to a composition comprising an organic solvent solution of an Ir(I) reagent, wherein the Ir(I) reagent is selected from the group consisting of:

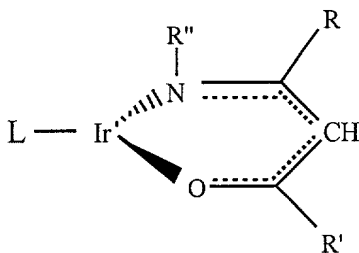
Lewis base stabilized Ir(I)  $\beta$ -diketonates of formula I:



## I

wherein R and R' may be alike or different and may be H, aryl, perfluoroaryl, C<sub>1</sub> - C<sub>6</sub>  
 5 alkyl, or C<sub>1</sub> - C<sub>6</sub> perfluoroalkyl, and L is a coordinating Lewis base; and

Lewis base stabilized Ir(I)  $\beta$ -ketoiminates of formula II:



## II

wherein R, R', and R'' are the same or different, and are independently selected from the  
 group consisting of H, aryl, perfluoroaryl, C<sub>1</sub> - C<sub>6</sub> alkyl, or C<sub>1</sub> - C<sub>6</sub> perfluoroalkyl, and  
 15 L is a coordinating Lewis base; and

the organic solvent solution comprises a non-polar solvent, such as C<sub>5</sub> - C<sub>12</sub> hydrocarbon  
 alkanes (e.g., hexane, heptane, octane, nonane and decane) and C<sub>6</sub> - C<sub>10</sub> hydrocarbon  
 aryls (e.g., benzene, toluene and xylene).

20 Yet another aspect of the invention relates to a composition comprising a non-polar  
 solvent solution of a cyclooctadiene (COD) adduct of an Ir(I) beta-diketonate.



Other aspects, features and embodiments of the invention will be more fully apparent from the ensuing disclosure and appended claims.

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**DETAILED DESCRIPTION OF THE INVENTION, AND PREFERRED  
EMBODIMENTS THEREOF**

10 The following patents and patent applications are hereby incorporated herein by reference in their respective entireties:

U.S. Patent Application No. 08/966,796 filed November 10, 1997 in the names of Thomas H. Baum and Frank DiMeo, Jr., for "Method for Etch Fabrication of Iridium-Based Electrode Structures;"

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U.S. Patent No. 5,840,897 issued November 24, 1998 in the names of Peter S. Kirlin, Duncan W. Brown, Thomas H. Baum, Brian A. Vaartstra and Robin A. Gardiner for "Metal Complex Source Reagents for Chemical Vapor Deposition;"

20 U.S. Patent Application No. 08/484,654 filed June 7, 1995 in the names of Robin A. Gardiner, et al. for "Method of Forming Metal Films on a Substrate by Chemical Vapor Deposition;"

United States Patent No. 5,820,664 issued October 13, 1998 in the names of Robin A. Gardiner et al. for "Precursor Compositions for Chemical Vapor Deposition, and Ligand Exchange Resistant Metal-Organic Precursor Solutions Comprising Same;"

- 5 United States Patent No. 5,204,314 issued April 20, 1993 in the names of Peter S. Kirlin, et al. for "Method for Delivering an Involatile Reagent in Vapor Form to a CVD Reactor;"

- 10 United States Patent 5,453,494 issued September 26, 1995 in the names of Peter S. Kirlin, et al. for "Metal Complex Source Reagents for MOCVD;"

United States Patent No. 5,916,359 issued June 29, 1999 in the names of Thomas H. Baum, et al. for "Alkane and Polyamine Solvent Compositions for Liquid Delivery Chemical Vapor Deposition;"

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United States Patent 5,923,970 issued July 13, 1999 in the names of Peter S. Kirlin, et al. for Method of Fabricating a Ferroelectric Capacitor with a Graded Barrier Layer Structure;" and

- 20 U.S. Patent No. 5,719,417 issued February 17, 1998 in the names of Jeffrey Roeder, et al. for "Ferroelectric Integrated Circuit Structure."

As used herein, the term "Lewis base" means a compound or chemical moiety that forms a bond by donating a pair of electrons. The compositions of the present invention containing the Lewis base constituent are understood to be devoid of other components that preclude the electron donor character of the Lewis base constituent from being present.

With respect to the Ir(I) precursor compositions of the invention, as hereinafter more fully described, it is to be appreciated that the compositions may be specifically characterized as comprising, consisting or consisting essentially of the constituents specifically referenced or described herein, and such compositions may specifically be characterized as being free, substantially free or devoid of any constituents not specifically referenced or described herein, as may be claimed hereinafter at any time during the proceedings involving the application hereof, or an application based hereon.

The present invention relates to the discovery that Ir-based electrode structures can be readily formed without the necessity of depositing the Ir component from a precursor or source material in a reducing atmosphere, as has heretofore been the approach and objective of the prior art.

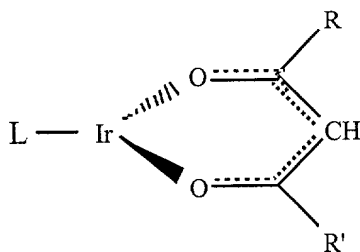
Contrariwise, the present invention contemplates a method of forming an iridium-containing film on a substrate, from an iridium-containing precursor thereof which is decomposed to deposit iridium on the substrate, in which the decomposition of the precursor and the deposition of iridium on the substrate is carried out in an oxidizing

ambient environment to deposit iridium in the form of iridium *per se* or in the form of iridium oxide.

Iridium may be deposited on the substrate in the method of the present invention in any suitable manner, including chemical vapor deposition, liquid delivery, sputtering, ablation, or any other suitable technique known in the art for deposition of metal on a substrate from a metal-organic or other precursor source material. Among the foregoing, chemical vapor deposition is preferred when the iridium-based structures being formed have critical dimensions below about 0.5 microns.

In the method of the invention, the precursor for the iridium component may be any suitable iridium precursor compound, complex or composition that is advantageous for yielding iridium for deposition on the substrate. The iridium precursor may for example comprise a Lewis base-stabilized  $\beta$ -diketonate iridium composition or a Lewis base-stabilized  $\beta$ -ketoiminate composition, of the formulae:

Lewis base stabilized Ir(I)  $\beta$ -diketonates of formula I:

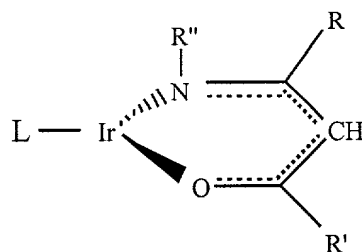


**I**

wherein R and R' are the same or different and may be H, aryl, perfluoroaryl, C<sub>1</sub> - C<sub>6</sub> alkyl, or C<sub>1</sub> - C<sub>6</sub> perfluoroalkyl, and L is a coordinating Lewis base, preferably alkene, diene, cycloalkene, cycloalkadiene, cyclooctatetraene, alkyne, substituted alkyne (symmetrical or asymmetrical), amine, diamine, triamine, tetraamine, ether, tetrahydrofuran, glyme, diglyme, triglyme, tetraglyme, phosphine, carbonyl, dialkyl sulfide, vinyltrimethylsilane, and allyltrimethylsilane,

or

10 Lewis base stabilized Ir(I)  $\beta$ -ketoiminates of formula II:



II

wherein R, R', and R'' are the same or different, and are independently selected from the group consisting of H, aryl, perfluoroaryl, C<sub>1</sub> - C<sub>6</sub> alkyl, or C<sub>1</sub> - C<sub>6</sub> perfluoroalkyl, and L is a coordinating Lewis base, preferably selected from the group consisting of alkene, diene, cycloalkene, cycloalkadiene, cyclooctatetraene, alkyne, substituted alkyne (symmetrical or asymmetrical), amine, diamine, triamine, tetraamine, ether,

tetrahydrofuran, glyme, diglyme, triglyme, tetraglyme, phosphine, carbonyl, dialkyl sulfide, vinyltrimethylsilane, and allyltrimethylsilane.

For the Lewis base in the above precursors of formulae I and II, one or more Lewis base molecules may be preferred, especially for ether, cyclic ether, tetrahydrofuran, alkene, alkyne, carbonyl and phosphine ligands. In some embodiments of precursors of formula II, R and R' may be identical and R" will be independently selected from the substituents listed above.

- 10 In CVD-based embodiments of the present invention, either a bubbler or organic solution liquid delivery can be utilized for the chemical vapor deposition of the Ir/IrO<sub>2</sub> thin film. The specific precursor may be suitably optimized for the delivery and transport of the precursor to the CVD reactor. The precursor is decomposed in the presence of an oxidant (e.g., O<sub>2</sub>, O<sub>3</sub>, or N<sub>2</sub>O) to preferentially deposit the metal Ir (< 500 °C) or the oxide, IrO<sub>2</sub> 15 (>550 °C). In some applications, the formation of a bi-layered Ir/IrO<sub>2</sub> film may be preferred.

The etching of Ir and IrO<sub>2</sub> in the practice of the invention, after the initial formation of the iridium-containing film, may be carried out with the use of halogen-based systems, such as chlorine, bromine, and fluorine based plasma or ion beam etch chemistries. The 20 formation of halogens of Ir(I) and Ir(III) can be exploited to etch and pattern electrodes for semiconductor and ferroelectric device applications. In systems where IrO<sub>2</sub> is present, the use of either a reducing pre-treatment (to return the iridium oxide to Ir metal) or the use of fluorine etchants may be preferred. The formation and removal of etch by-

products depends on the volatility of the halide species. The addition of stabilizing co-reactants may usefully be employed to facilitate the removal and etching of the materials.

The iridium-containing films deposited in accordance with the method of the present invention may be etched with a dry etch method, as more fully described in the  
5    aforementioned co-pending U.S. Patent Application No. 08/966,796 filed November 10, 1997 in the names of Thomas H. Baum and Frank DiMeo Jr. for "Method for Etch Fabrication of Iridium-Based Electrode Structures," optionally using specific chemical enhancements to the rate of etching. The addition of carbon monoxide,  
10   trifluorophosphine, or trialkyl phosphines can accelerate the rate of etching by enhancing the volatility of the produced etch by-products.

For example, in the etching of the Ir-containing film on the substrate, the removal rate for the process may be advantageously accelerated by the presence of carbon monoxide  
15   (CO). The rates are strongly dependent upon the gas-phase partial pressure of the reactants in elevated substrate temperature regimes (e.g., 725 - 975 °C). The presence of CO may serve to enhance the reactant volatility through the formation of  $(\text{CO})_y\text{IrX}_3$  (where X = Cl, Br) and for  $\text{Ir}(\text{Cl})_4$ .  $\text{IrF}_6$  may also be employed for such purpose. These materials can be used advantageously for etching Ir in halogen-based plasmas, ion beams  
20   and in hybrid etching schemes.

In some instances, it may be desirable to convert the iridium oxide material deposited on the substrate to a pure iridium metal for a specific fabrication or device application. In

such instance, the deposited film of iridium oxide may be exposed to a reducing gas, such as hydrogen, forming gas, CO, ROH, etc. to effect such conversion.

After its formation and any additional patterning, the iridium-containing electrode may  
5 have deposited thereon a high temperature dielectric and/or ferroelectric material in the same oxidizing ambient environment employed for the deposition of the iridium-containing film.

It is therefore unnecessary to purge the chamber of a reducing atmosphere, or to transfer  
10 the substrate article bearing the iridium-containing film from the iridium deposition chamber to a dielectric/ferroelectric deposition chamber, as has been done in the prior art to accommodate the usage of hydrogen or other reducing gas (forming gas) atmospheres in the iridium electrode formation step. The method of the invention therefore achieves a substantial simplification of the procedure for forming a capacitor or other  
15 microelectronic device in which the iridium-containing electrode is overcoated with a dielectric or ferroelectric material.

The iridium films deposited in the practice of the present invention may therefore be utilized for the formation of electrode and other elements of semiconductor devices, such  
20 as for example DRAMs, FRAMs, hybrid systems, smart cards and communication systems, as well as any other applications in which the thin films of iridium and/or iridium oxide are advantageously employed, such as catalytic systems.



As a specific example of an Ir(I) precursor composition that may be usefully employed in the broad practice of the present invention, a precursor comprising a cycloalkenyl moiety such as the cyclooctadiene (COD) adduct of Ir(I) beta-diketonate, in a suitable hydrocarbon solvent (octane), may be employed for liquid delivery MOCVD of iridium  
5 for electrode formation in the manufacture of semiconductor devices such as those mentioned in the preceding paragraph.

The hydrocarbon solvents useful for such purpose may for example include non-polar solvents, e.g., C<sub>5</sub>-C<sub>12</sub> alkane solvents such as hexane, octane, etc., C<sub>1</sub>-C<sub>8</sub> alkyl-substituted  
10 benzene solvents, e.g., toluene, xylene or other hydrocarbon substituted solvents, e.g., diethylether or tetrahydrofuran, etc.. The solvent medium for the cyclooctadiene (COD) adduct of Ir(I) beta-diketonate desirably does not include alkanolic solvents (e.g., ethanol and isopropanol) since the presence of such solvents may lead to premature decomposition of the source reagent complex in solution, prior to delivery or during  
15 vaporization of the precursor mixture.

As used herein, the term "non-polar solvent" means a hydrocarbon alkane(s) or aryl solvent having a dielectric constant ( $\epsilon$ ) of from about 1 to about 15. The dielectric constant provides a measure of the solvent's capacity to separate ionic charge, so that the  
20 higher the value of the dielectric constant, the more polar the solvent. Further, these solvents are characterized by being primarily hydrocarbons without the presence of polar substituents, such as oxygen, sulfur, chlorine, bromine, fluorine and other polarizable groups.

The  $\beta$ -diketonate ligand in such cyclooctadiene (COD) adduct of Ir(I)  $\beta$ -diketonate may be of any suitable type, including the illustrative  $\beta$ -diketonate ligand species set out in Table A below:

<b>Table A</b>	
<b>Beta-diketonate ligand</b>	<b>Abbreviation</b>
2,2,6,6-tetramethyl-3,5-heptanedionato	thd
1,1,1-trifluoro-2,4-pentanedionato	tfac
1,1,1,5,5,5-hexafluoro-2,4-pentanedionato	hfac
6,6,7,7,8,8-heptafluoro-2,2-dimethyl-3,5-octanedionato	fod
2,2,7-trimethyl-3,5-octanedionato	tod
1,1,1,5,5,6,6,7,7,7-decafluoro-2,4-heptanedionato	dfhd
1,1,1-trifluoro-6-methyl-2,4-heptanedionato	tfmhd

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The liquid delivery MOCVD system for growing the iridium films of the invention may for example comprise a system of the type disclosed in U.S. Patent 5,204,314 issued April 20, 1993 to Peter S. Kirlin et al. and in U.S. Patent 5,536,323 issued July 16, 1996 to Peter S. Kirlin et al., which utilize heated vaporization structures such as microporous disk elements to effect high rate vaporization of the source reagent materials for the metal oxide film. In operation, liquid source reagent compositions are flowed onto the vaporization structure for flash vaporization. Vapor thereby is produced for transport to the deposition zone, e.g., the CVD reactor. The liquid delivery systems of these patents provide high efficiency generation of vapor from which films may be grown on substrates.

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The precursor vapor formed by the high rate vaporization apparatus is transported to a chemical vapor deposition zone containing a substrate, e.g., a wafer provided on a heated susceptor. Upon contacting of the precursor vapor with the wafer, the metal components of the vapor are deposited on the wafer surface. The vapor may be delivered in the chemical vapor deposition chamber by a disperser such as a showerhead or nozzle, to provide a uniform flux of the vapor across the width of the wafer, to yield a correspondingly uniform thickness of deposited metal-containing film on the wafer. The process conditions (temperature, pressure, flow rate and composition of the vapor) may be suitably controlled to ensure an optimum process result for the specific MOCVD operation being conducted in the process system.

While the invention has been variously disclosed herein with reference to illustrative embodiments and features, it will be appreciated that the embodiments and features described hereinabove are not intended to limit the invention, and that other variations, modifications and other embodiments will suggest themselves to those of ordinary skill in the art. The invention therefore is to be broadly construed, consistent with the claims hereafter set forth.